REMARKS

I. Status Summary

Claims 1-7 are currently pending. Claim 1 is amended herein. Therefore, upon entry of this Amendment, Claims 1-7 will be pending.

Element (b) of Claim 1 has been amended to include the phrase "wherein the base address register is switched through by the first multiplexer circuit". Support for the amendment to Claim 1 can be found throughout the subject application, particularly at page 5, lines 15-25, and page 5, line 36, to page 6, line 5.

II. Claim Rejections Under 35 U.S.C. § 102

Claims 1-7 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,287,465 to Kurosawa et al. (hereinafter, "Kurosawa"). This rejection is respectfully traversed.

Upon careful consideration and review of Kurosawa, applicant respectfully submits that Kurosawa does not disclose each and every element of amended Claim 1 and therefore does not anticipate the claim. Claim 1 recites a data processing method using a multiplicity of processors which operate in parallel and to which a respective command for data processing is supplied simultaneously. Further, Claim 1 has been amended to recite that at least one of the processors is supplied with a condition command which conditions the parallel execution of all the commands for data processing. Summarily, Kurosawa does not disclose that the commands for

data processing and the condition command are supplied simultaneously to a multiplicity of processors, or that the condition command conditions the parallel execution of all the commands for data processing, as required by Claim 1.

Kurosawa teaches a parallel processing apparatus comprising a program counter for indicating an instruction to be read out, m instruction registers for storing indicated instructions therein, arithmetic units sharing the register file and executing an arithmetic operation, plural instructions parallel processing means for reading consecutive instructions from an address indicated by the program counter, conditional branch instruction processing means. (Kurosawa, column 4, line 61, to column 5, line 3.) The condition branch instruction processing means has a condition branch instruction and an unconditional branch instruction. (Kurosawa, column 5, lines 3-5.) In this configuration, m instructions are first read out and decoded to check if there is a branch instruction in the k-th instruction. (Kurosawa, column 5, lines 16-19.) Further, Kurosawa teaches that first to (k+1)th instructions are executed if the k-th instruction is a branch instruction. (Kurosawa, column 5, lines 16-34.) Teferring to lines 22-25 of column 20, Kurosawa teaches that, at least with the branch instruction, the next instruction is executed irrespective of the satisfaction or nonsatisfaction of a condition. Thus, Kurosawa fails to teach that all commands for data processing are conditioned by the condition command, as required by Claim 1.

Further, Kurosawa teaches that the execution of the (k+2)th to the m-th instructions is prevented if there is a branch instruction in the k-th instruction. (Kurosawa, column 5, lines 16-22.) If the condition fails, it remains unclear whether

these last instructions are executed in parallel with the first instructions. In addition, the figures of <u>Kurosawa</u> suggest that the (k+2)th to the m-th instructions will always be executed after the k-th and the (k+1)th instructions, not in parallel. Applicant respectfully submits that nowhere does <u>Kurosawa</u> teach that a condition command conditions the parallel execution of all the commands for data processing, as required by Claim 1. Thus, <u>Kurosawa</u> cannot anticipate Claim 1. Applicant therefore respectfully submits that the rejection of Claim 1 under 35 U.S.C. § 102(b) be withdrawn and the claim allowed at this time.

Claims 2-7 depend from Claim 1. Therefore, Claims 2-7 includes the features of Claim 1. Thus, the comments presented above relating to Claim 1 apply equally to Claims 2-7. Therefore, Claims 2-7 are believed to be patentably distinguished over <u>Kurosawa</u>. Applicant respectfully requests that the rejections of Claims 2-7 under 35 U.S.C. § 102(b) be withdrawn and the claims allowed at this time.

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CONCLUSION

In light of the above amendments and remarks, it is respectfully submitted that the present application is now in proper condition for allowance, and an early notice to such effect is earnestly solicited.

If any small matter should remain outstanding after the Patent Examiner has had an opportunity to review the above Remarks, the Patent Examiner is respectfully requested to telephone the undersigned patent attorney in order to resolve these matters and avoid the issuance of another Official Action.
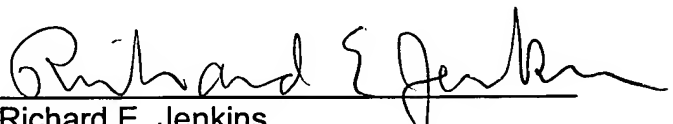
DEPOSIT ACCOUNT

The Commissioner is hereby authorized to charge any fees associated with the filing of this correspondence to Deposit Account No. **50-0426**.

Respectfully submitted,

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